



THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:  
**GARNIER ET AL.**

Serial No. 09/499,060

Filing Date: **February 4, 2000**

For: **VOLTAGE RAMP GENERATOR AND  
CURRENT RAMP GENERATOR  
INCLUDING SUCH A GENERATOR**

) Examiner: T. Cunningham

) Art Unit: 2816

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**Kevin Peterson**  
(TYPED OR PRINTED NAME OF PERSON MAILING PAPER OR FEE)

PRELIMINARY AMENDMENT

**Kevin L. Peterson**  
(SIGNATURE OF PERSON MAILING PAPER OR FEE)

Director, U.S. Patent and Trademark Office  
Washington, D.C. 20231

Sir:

Prior to the calculation of fees and examination of  
the present application, please enter the amendments and  
remarks set out below.

In the Claims:

Please amend the claims as follows:

9. (Twice Amended) An integrated circuit voltage  
ramp generator comprising:

a semiconductor substrate;

a capacitance on said semiconductor substrate; and

a charging circuit on said semiconductor substrate  
and connected to said capacitance and comprising

a current generator having a first resistance,  
and

a circuit connected to said current generator  
and to said capacitance having a second resistance  
and enabling a capacitance charging current to be

#14/D  
5/1/02  
Smith

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